

**INFORMATION DISCLOSURE CITATION**  
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ATTY DOCKET NO.  
**LOT920030015US1**

SERIAL NO.

Applicant(s): **John D. Curtis**

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**2188**

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
<b>JS</b>	US 2002/0143892 A1	10/2002	Mogul			
	US 2002/0133537 A1	09/2002	Lau et al.			
	US 2002/0073167 A1	06/2002	Powell et al.			
	US 2002/0078300 A1	06/2002	DHARAP			
	US 2002/0065809 A1	05/2002	Kitayama			
	US 2001/0056416 A1	12/2001	Garcia-Luna-Aceves			
	US 6,542,967 B1	04/2003	Major			
	US 6,360,273 B1	03/2002	Beurket et al.			
	US 2003/0051101 A1	03/2003	Burger et al.			
	US 2003/0009538 A1	01/2003	Shah et al.			
	US 2001/0054084 A1	12/2001	Kosmynin			

**FOREIGN PATENT DOCUMENTS**

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

<b>JS</b>	Kanada, "Two Rule-Based Building-Block Architectures for Policy-Based Network Control", Undated, pp. 1-10.
<b>JS</b>	Haas et al., "Loading a Cache with Query Results", Proceedings of the 25th VLDB Conference, Edinburgh Scotland, 1999.

EXAMINER

*Jasmine Song*

DATE CONSIDERED

**9/22/05**

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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JS	US 2001/0043600 A1	11/2001	Chatterjee et al.			
JS	US 6,363,397 B1	03/2002	Kitayama			

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						YES	NO

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

JS		Patterson et al., "Informed Prefetching and Caching", 12/1995, pp. 79-95.

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